

Editorial

We present the third issue of the thirteenth volume of the **Journal of Intelligent Computing** with the papers listed below.

In the opening paper on “**Review of association memory methods and usability**”, the authors have presented the review of association memory methods and their usability with a note on cabinets systems used. Besides, they have also detailed the system of managing elements for file cabinets. Finally, they have implemented the system.

In the next paper on “**Geographical Information System data handling with open multicore processing**”, the authors have presented a multicore architecture to support the geospatial data management. For the multicore processing and using Geographic Information Systems, they have used parallel processing based on the application of Open Multi Processing. They have conducted testing and found that the high performance computing in geographical information system.

In the last paper on “**VLSI testing using FPGA devices**” the authors have used parallel LFSR to experiment the intellectual property blocks with VLSI. The PLFSR is input is linked to apply in test mod test vectors which, possible, detect faults. The proposed PLFSR is implemented FPGA device, and perform at 200 MHz clock frequency. The system proposed is experimented in real conditions. We hope the papers of this issue may generate more interest.

Editors