Editorial

We bring the first issue of this volume of the **Journal of Information Organization** with the research described below.

In the opening paper, "**Formal verification of SystemC using error-free translation**," the authors used SystemC/TLMs to simulate embedded software. In the system-C/TML program, transitions are obtained by running the original code using G++ and an extended systemC library. The proposed system is an open-source simulator without formal verification tools.

In the next paper, "**Model and Instance Diagrams for Meta-modelling and model-driven engineering**", the authors proposed model and instance diagrams, or equivalently class and object diagrams, which become the first-level entities in a well-expressed programming language. The proposed semantics of diagrams are compositional and self-describing in nature. The authors claim that Encoding diagrams into type theory make them immediately useful.

In the last paper, "**Computational offloading for real-time tasks in Embedded Systems**," the authors described computational offloading for real-time tasks in embedded systems and developed a pseudo-projective-time algorithm to derive feasible schedules if they exist. The authors extended their algorithms to minimize the periods/ relative deadlines of the tasks for maximum performance.

We hope these papers are interesting to read.

Editors