Editorial

We bring the first issue of the fifteenth volume of the **Electronic Devices** with the below papers.

In the paper, *"A Novel Interleaved Bridgeless SEPIC Converter with Synchronous Rectifier for multiple applications"*, the authors to enhance the power changeover operation a modified multilevel inverter is connected through an isolated $(LC)^2$ Synchronous rectifier in the proposed system. They have introduced zero voltage switching (ZVS) to the synchronous rectifier to reduce the device's operational losses. To validate the proposed system they have deployed MATLAB/Simulink for the verification of theoretical analyses and an FPGA controller is used as the experimental setup.

The second paper, "*Development of Parameterized Verilog-AMS Model of Photovoltaic Cells*", the authors developed a parameterized Verilog-AMS model and panel for photovoltaic cells. They have presented Definitions for obtaining the model characteristics from the simulation results. In the Dolphin Integration SMASH environment, the model is tested.
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In the technical report on *"EC Lab with plugin board"* a presentation on HDL platform is available which is convenient and cost-effective pocket HDL platform for Embedded Electronic Design. The Ekalavya is an open-source electronics HDL platform that provides seamless implementation with compatible breakout boards and plugins using VHDL/Verilog. It has applications in Edge computing for AI, ML, Mechatronics, Robotics and Industrial automation where the appropriate breakout board which in combination with the main EC lab unit will fuel the operations.

We will bring more research in the forthcoming issues.

Editors